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	Application No.	Applicant(s)	-
Notice of Allowability	10/721,361	LEE, PHAL JIN	
	Examiner	Art Unit	
	Tarifur R. Chowdhury	2871	
The MAILING DATE of this communication appears All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this a or other appropriate communication IGHTS. This application is subject and MPEP 1308.	pplication. If not included on will be mailed in due course. THIS	ve
1. This communication is responsive to <u>election filed on 06/20</u>	<u>0/05</u> .		
2. ☑ The allowed claim(s) is/are <u>1-14</u> .			
 Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have 1. Copies of the certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have 1. All the priority documents have 3. Copies of the priority documents have 1. A Copies of the priority documents have 1. Copies of the priority document	e been received. e been received in Application No. cuments have been received in thi of this communication to file a repl IENT of this application. itted. Note the attached EXAMINE es reason(s) why the oath or decla	s national stage application from the y complying with the requirements	
 (a) ☐ including changes required by the Notice of Draftspers 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☐ including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1) 	on's Patent Drawing Review(PTC s Amendment / Comment or in the	Office action of	
each sheet. Replacement sheet(s) should be labeled as such in t			
 DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT 			
Attachment(s) 1. ⊠ Notice of References Cited (PTO-892)	5 Notice of Informal	Patent Application (PTO-152)	
 Notice of References Cited (PTO-892) Notice of Draftperson's Patent Drawing Review (PTO-948) 	6. Interview Summar	y (PTO-413),	
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail D 08), 7. Examiner's Amen	ate dment/Comment	
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit		nent of Reasons for Allowance	
of Biological Material	9. Other		

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Election/Restrictions

1. Claims 1 and 11 are generic and allowable. Accordingly, the restriction requirement as to the encompassed species is hereby withdrawn and claims 6, 14 and 8, directed to the species no longer withdrawn from consideration since all of the claims to this species depend from or otherwise include each of the limitations of an allowed generic claim.

Allowable Subject Matter

- 2. Claims 1-14 are allowed.
- 3. The following is an examiner's statement of reasons for allowance:
- 4. As to claim 1, the prior arts of record do not anticipate or render obvious to one skilled in the art an LCD assembly for a home appliance comprising an insulator provided rectangular at one side of the home appliance to have a guide along an edge of the insulator; a printed circuit board loaded inside the guide; a coating layer on the printed circuit board; an LCD panel provided over the coating layer; and a plurality of recesses formed along edges of the insulator, the printed circuit board, the coating layer, and the LCD panel.
- 5. As to claim 11, the prior arts of record do not anticipate or render obvious to one skilled in the art an LCD assembly printed circuit board provided with a dummy connected thereto through a multitude of bridges in a recess at an edge thereof, a printed circuit board of a home appliance LCD assembly wherein the bridges extend outside the recess.
- 6. USPAT 5,161,009 discloses an IC module with a folding junction structure.

 USPAT 5,743,748 discloses an emulator coupling device wherein a board is electrically coupled with the in-circuit emulator, to the dummy via connectors, which are to be

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coupled with the relay connectors. USPAT 6,094,359 discloses an HF housing comprising an arrangement for electrically interconnecting at least two HF conductor tracks which are formed on adjacent printed circuit boards, which are separated from each other by a slit, and whose ends are situated opposite each other in the region of the slit and an auxiliary printed circuit board connecting the conductor tracks. USPAT 6,580,620 discloses a matrix type printed circuit board for semiconductor packages. JP 11-121892 discloses a flexible circuit board connected to glass substrate of LCD having dummy patterns formed between the connecting terminals and adhesive agent accommodation recess is formed on connecting terminals. KR 2002023065 (commonly assigned) discloses a display part of a washing machine including an LCD connected to a flexible circuit board for displaying an image data. KR 2002023066 (commonly assigned) discloses a protection device of display part of washing machine wherein the protection casing includes a plurality of hooks at a lower end part to be coupled with a PCB mounted to the controller manipulation part. KR 2002022980 (commonly assigned) an LCD for a washing machine including a PCB. However, all of the prior arts alone or in combination with any other prior arts fail to teach or suggest the claimed home appliance LCD assembly including a printed circuit board that is provided with a dummy connected thereto through a multitude of bridges in a recess at an edge thereof, wherein the bridges extend outside the recess or that an LCD assembly for a home appliance comprising an insulator provided rectangular at one side of the home appliance to have a guide along an edge of the insulator; a printed circuit board loaded inside the guide; a coating layer on the printed circuit board; an LCD panel provided

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over the coating layer; and a plurality of recesses formed along edges of the insulator, the printed circuit board, the coating layer, and the LCD panel.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tarifur R. Chowdhury whose telephone number is (571) 272-2287. The examiner can normally be reached on M-Th (6:30-5:00) Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571) 272-2293. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TRC September 05, 2005